

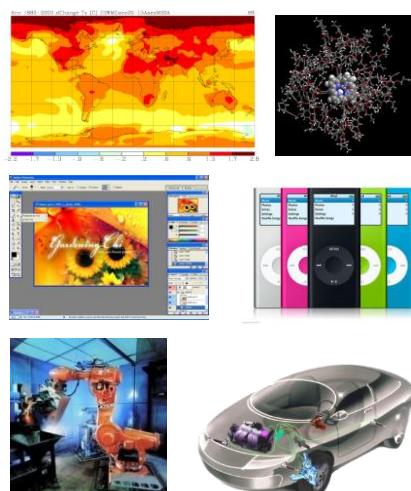
Exploiting Domain-Specific Knowledge: Spiral

Markus Püschel & Georg Ofenbeck

Computer Science
ETH zürich



Mathematical Computing



Science simulations

Audio, image, Video processing

Signal processing, communication, control

Security

Machine learning, data analytics

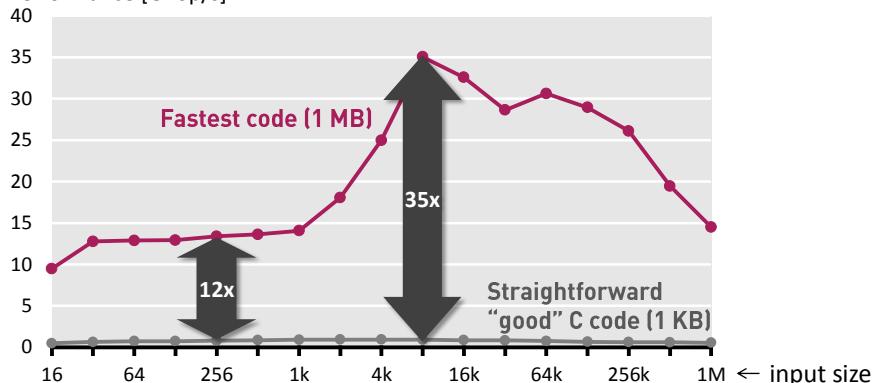
Optimization

***Highest performance
is often crucial***

Example: Discrete Fourier Transform

DFT (single precision) on Intel Core i7 (4 cores, 2.66 GHz)

Performance [Gflop/s]

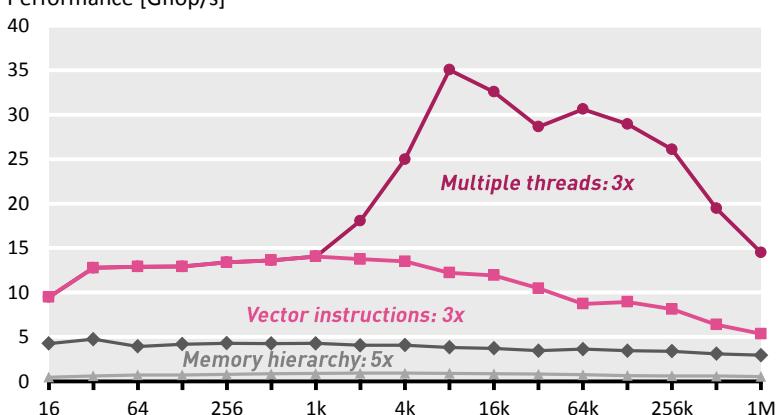


Vendor compiler, best flags

Roughly same operations count

DFT (single precision) on Intel Core i7 (4 cores, 2.66 GHz)

Performance [Gflop/s]



Compiler doesn't do the job

Doing by hand = restructure algorithm for locality & parallelism,
handle choices, choose proper code style, use vector intrinsics,
= nightmare

Optimization: Register Locality and ILP

```
// straightforward code
for(i = 0; i < N; i += 1)
  for(j = 0; j < N; j += 1)
    for(k = 0; k < N; k += 1)
      c[i][j] += a[i][k]*b[k][j];
```

Concise and slow

Removes aliasing

Enables register allocation and instruction scheduling

Compiler does not do well:

- often illegal
- many choices

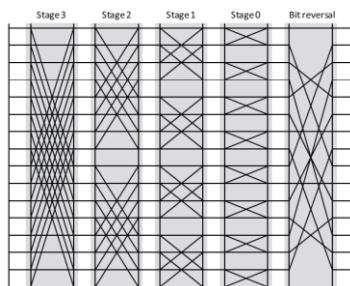
```
// unrolling + scalar replacement
for(i = 0; i < N; i += MU) {
  for(j = 0; j < N; j += NU) {
    for(k = 0; k < N; k += KU) {
      t1 = A[i*N + k];
      t2 = A[i*N + k + 1];
      t3 = A[i*N + k + 2];
      t4 = A[i*N + k + 3];
      t5 = A[(i + 1)*N + k];
      <more copies>
    }
  }
}
```

```
t10 = t1 * t9;
t17 = t17 + t10;
t21 = t21 * t8;
t18 = t18 + t21;
t12 = t5 * t9;
t19 = t19 + t12;
t13 = t5 * t8;
t20 = t20 + t13;
<more ops>
```

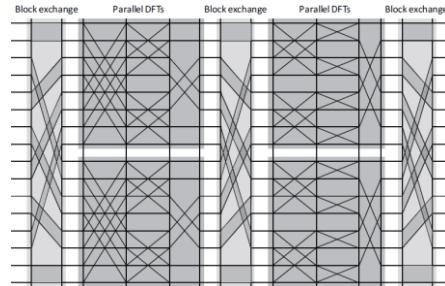
```
C[i*N + j]          = t17;
C[i*N + j + 1]      = t18;
C[(i+1)*N + j]      = t19;
C[(i+1)*N + j + 1] = t20;
```

Ugly and fast

Optimization for Parallelism (Threads)



Parallelism is present, but is not in the “right shape”

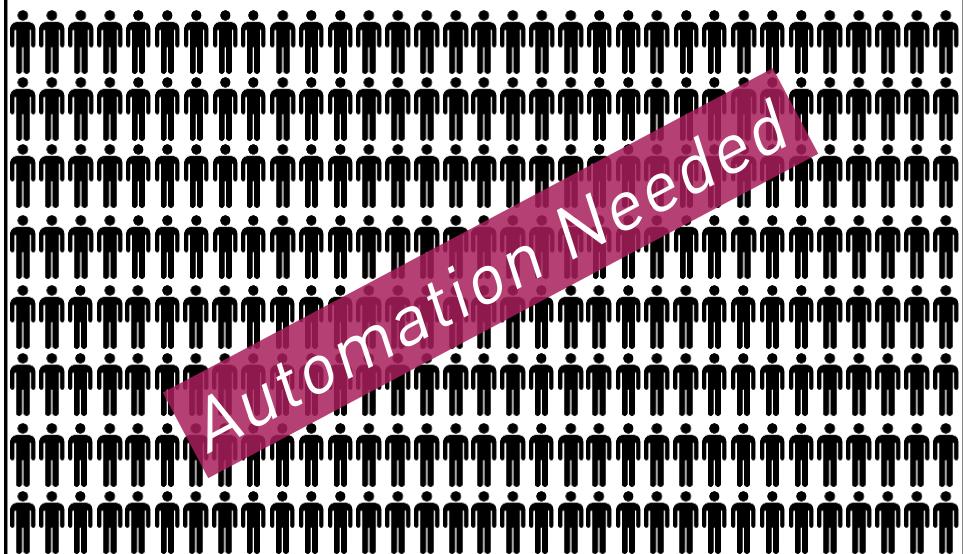


Restructured for locality and parallelism (shared memory, 2 cores, 2 elements per cache line)

Compiler usually does not do

- analysis may be unfeasible
- may require algorithm changes
- may require domain knowledge
- may require processor parameters

Current practice: Thousands of programmers re-implement and re-optimize the same functionality for every new processor and for every new processor generation



Goal:

Computer generation of high performance code for ubiquitous performance-critical components

Generate Code



"click"

Select convolutional code
Select a preset code or customize parameters

<input type="radio"/> custom	rate	1 / <input type="text" value="2"/>	code rate (?)
<input checked="" type="radio"/> Voyager	K	<input type="text" value="7"/>	constraint length (?)
<input type="radio"/> NASA-DSN	polynomials	<input type="text" value="109"/>	polynomials for the code in decimal notation (?)
<input type="radio"/> CCSDS/NASA-GSFC		<input type="text" value="79"/>	
<input type="radio"/> WiMax			
<input type="radio"/> CDMA IS-95A			
<input type="radio"/> LTE (3GPP - Long Term Evolution)			
<input type="radio"/> UWB (802.15)			
<input type="radio"/> CDMA 2000			
<input type="radio"/> Cassini			
<input type="radio"/> Mars Pathfinder & Stereo			

Select implementation options

frame length	<input type="text" value="2048"/>	unpadded frame length	
Vectorization level	<input type="text" value="scalar C"/>	type of code (?)	

Viterbi Decoder

DFT IP Cores

parameter	value	range	explanation
Problem specification			
transform size	<input type="text" value="64"/>	4-32768	Number of samples (?)
direction	<input type="text" value="forward"/>		forward or inverse DFT (?)
data type	<input type="text" value="fixed point"/>		fixed or floating point (?)
	<input type="text" value="16"/>	4-32 bits	fixed point precision (?)
	<input type="text" value="bits"/>		scaling mode (?)
Parameters controlling implementation			
architecture	<input type="text" value="fully streaming"/>		iterative or fully streaming (?)
radix	<input type="text" value="2"/>	2, 4, 8, 16, 32, 64	size of DFT basic block (?)
streaming width	<input type="text" value="2"/>	2-64	number of complex words per cycle (?)
data ordering	<input type="text" value="natural in / natural out"/>		natural or digit-reversed data order (?)
BRAM budget	<input type="text" value="1000"/>		maximum # of BRAMs to utilize (-1 for no limit) (?)

[@ www.spiral.net](#)

Possible Approach:

Capturing algorithm knowledge:
Domain-specific languages (DSLs)

Structural optimization:
Rewriting systems

High performance code style:
Compiler

Decision making for choices:
Machine learning

Spiral: Program Generation for Performance (www.spiral.net)



Franz Franchetti
Yevgen Voronenko
Jianxin Xiong
Bryan Singer
Srinivas Chellappa
Frédéric de Mesmay
Peter Milder
José Moura
David Padua
Jeremy Johnson
James Hoe
<many more>

funding: DARPA, NSF, ONR, Intel

Linear Transforms

$$\begin{pmatrix} y_0 \\ y_1 \\ \vdots \\ y_{n-1} \end{pmatrix} = y = Tx \longleftrightarrow T \cdot \begin{pmatrix} x_0 \\ x_1 \\ \vdots \\ x_{n-1} \end{pmatrix}$$

Output *Input*

Example: $T = \text{DFT}_n = [e^{-2k\ell\pi i/n}]_{0 \leq k, \ell < n}$

Algorithms: Example FFT, n = 4

Fast Fourier transform (FFT):

$$\begin{bmatrix} 1 & 1 & 1 & 1 \\ 1 & i & -1 & -i \\ 1 & -1 & 1 & -1 \\ 1 & -i & -1 & i \end{bmatrix} x = \begin{bmatrix} 1 & . & 1 & . \\ . & 1 & . & 1 \\ 1 & . & -1 & . \\ . & 1 & . & -1 \end{bmatrix} \begin{bmatrix} 1 & . & . & . \\ . & 1 & . & 1 \\ . & . & 1 & . \\ . & . & . & i \end{bmatrix} \begin{bmatrix} 1 & 1 & . & . \\ 1 & -1 & . & . \\ . & . & 1 & 1 \\ . & . & 1 & -1 \end{bmatrix} \begin{bmatrix} 1 & . & . & . \\ . & 1 & . & . \\ . & . & 1 & . \\ . & . & . & 1 \end{bmatrix} x$$

12 adds, 4 mults

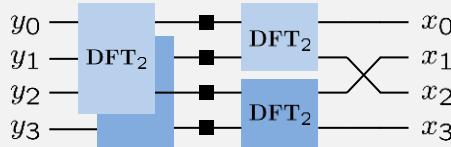
4 adds

1 mult

4 adds

0 adds/mults

Data flow graph



Description with matrix algebra (SPL)

$$\text{DFT}_4 = (\text{DFT}_2 \otimes \text{I}_2) \text{T}_2^4 (\text{I}_2 \otimes \text{DFT}_2) \text{L}_2^4$$

Decomposition Rules (>200 for >40 Transforms)

$$\begin{aligned} \text{DFT}_n &\rightarrow P_{k/2,2m}^\top (\text{DFT}_{2m} \oplus (I_{k/2-1} \otimes_i C_{2m} \text{rDFT}_{2m}(i/k))) (\text{rDFT}'_k \otimes I_m), \quad k \text{ even}, \\ \text{rDFT}'_k &= (P_{k/2,m}^\top \otimes I_2) \left(\begin{array}{c|c} \text{rDFT}'_{2m} & \\ \hline \text{rDFT}'_{2m} & \oplus \left(I_{k/2-1} \otimes_i D_{2m} \begin{array}{c|c} \text{rDFT}_{2m}(i/k) & \\ \hline \text{rDFT}_{2m}(i/k) & \text{rDFT}_{2m}(i/k) \end{array} \right) \end{array} \right) \left(\begin{array}{c|c} \text{rDFT}'_k & \\ \hline \text{rDFT}'_k & \otimes I_m \end{array} \right), \quad k \text{ even}, \\ \text{rDFT}_{2m}(i/k) &= L_m^{2m} (I_k \otimes \begin{bmatrix} \text{rDFT}_{2m}((i+u)/k) & \\ \text{rDHT}_{2m}((i+u)/k) & \end{bmatrix}) \left(\begin{bmatrix} |\text{rDFT}_{2b}(u)| & \\ |\text{rDHT}_{2b}(u)| & \end{bmatrix} \otimes I_m \right), \quad k \text{ even}, \\ \text{rDFT}'_{2m} &= (Q_{k/2,2m}^\top \otimes I_2) (I_k \otimes \text{rDFT}_{2m}(i+1/2/k)) (\text{rDFT}'_{3k} \otimes I_m), \quad k \text{ even}, \\ \text{DCT-2}_n &\rightarrow P_{k/2,2m}^\top (\text{DCT-2}_{2m} \otimes (I_{k/2-1} \otimes_i V_{2m} \text{rDFT}'_{2m} \text{rDFT}'_{2m}^\top)) P_{k/2,2m} (\text{rDFT}'_{2m} \otimes I_m) Q_{m/2,k} \end{aligned}$$

Rules = algorithm knowledge

$$\begin{aligned} \text{DFT}_n &\rightarrow (\text{DFT}_k \otimes \text{I}_m) \text{L}_m^n (\text{I}_k \otimes \text{DFT}_m) \text{L}_m^n, \quad n = km \\ \text{DFT}_n &\rightarrow P_{k/2,m}^\top ((\text{I}_k \otimes \text{DFT}_{k-1}) \otimes_i (\text{I}_m \otimes \text{DFT}_{m-1})) R_{k/2,m} \quad k \text{ prime} \\ \text{DCT-3}_n &\rightarrow (\text{I}_m \oplus \text{J}_m) \text{L}_m^n (\text{DCT-3}_m(1/4) \oplus \text{DCT-3}_m(3/4)) \\ &\quad (\text{F}_2 \otimes \text{I}_m) \begin{bmatrix} \text{I}_m & 0 \oplus \text{J}_{n-1} \\ 0 \oplus \text{J}_m & \frac{1}{\sqrt{2}}(\text{I}_1 \oplus 2\text{J}_m) \end{bmatrix}, \quad n = 2m \\ \text{DCT-4}_n &\rightarrow S_n \text{DCT-2}_n \text{diag}_{0 \leq k < n} (1/(2 \cos((2k+1)\pi/4n))) \\ \text{IMDCT}_{2m} &\rightarrow (\text{J}_m \oplus \text{I}_m \oplus \text{J}_m) \left(\begin{bmatrix} 1 & \\ -1 & \end{bmatrix} \otimes \text{I}_m \right) \oplus \left(\begin{bmatrix} -1 & \\ -1 & \end{bmatrix} \otimes \text{I}_m \right) \text{J}_{2m} \text{DCT-4}_{2m} \\ \text{WHT}_{2^k} &\rightarrow \prod_{i=1}^t (\text{I}_{2^{k_1+\dots+k_{i-1}}} \otimes \text{WHT}_{2^{k_i}} \otimes \text{I}_{2^{k_{i+1}+\dots+k_t}}), \quad k = k_1 + \dots + k_t \\ \text{DFT}_2 &\rightarrow \text{F}_2 \\ \text{DCT-2}_2 &\rightarrow \text{diag}(1, 1/\sqrt{2}) \text{F}_2 \\ \text{DCT-4}_2 &\rightarrow J_2 \text{R}_{13\pi/8} \end{aligned}$$

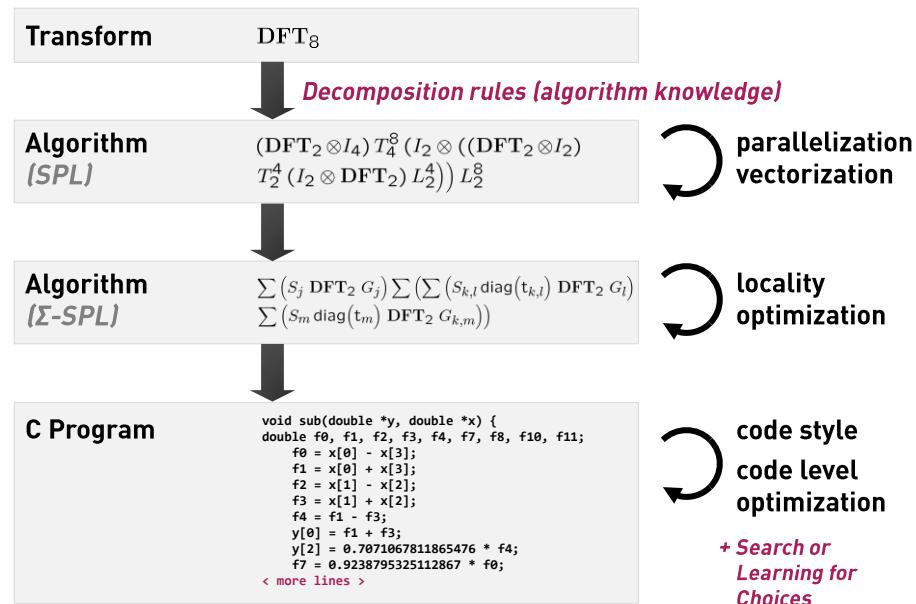
SPL to Code

SPL S Pseudo code for $y = Sx$	
$A_n B_n$	<code for: $t = Bx$ <code for: $y = At$
$I_m \otimes A_n$	for ($i=0; i < m; i++$) <code for: $y[i:n:1:i*n+n-1] = A(x[i:n:1:i*n+n-1])$
$A_m \otimes I_n$	for ($i=0; i < n; i++$) <code for: $y[i:n:i+m*n-n] = A(x[i:n:i+m*n-n])$
D_n	for ($i=0; i < n; i++$) $y[i] = D[i]*x[i];$
L_k^{km}	for ($i=0; i < k; i++$) for ($j=0; j < m; j++$) $y[i*m+j] = x[j*k+i];$
F_2	$y[0] = x[0] + x[1];$ $y[1] = x[0] - x[1];$

$$I_m \otimes A_n = \begin{bmatrix} A_n & & \\ & \ddots & \\ & & A_n \end{bmatrix}$$

Gives reasonable, straightforward code

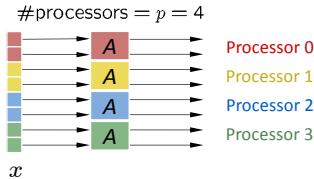
Program Generation in Spiral



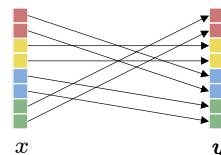
SPL to Shared Memory Code: Basic Idea

“Good” SPL structures

$$y = (I_p \otimes A)x$$



$$y = (P \otimes I_\mu)x$$



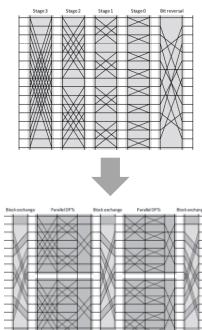
Rewriting: Bad structures good structures

} cache block size = $\mu = 2$

Example: SMP Parallelization

Franchetti, Voronenko & P, SC 2006

$$\begin{aligned}
 \underbrace{\text{DFT}_{mn}}_{\text{smp}(p,\mu)} &\rightarrow \underbrace{\left((\text{DFT}_m \otimes I_n) T_n^{mn} (I_m \otimes \text{DFT}_n) L_m^{mn} \right)}_{\text{smp}(p,\mu)} \\
 &\dots \\
 &\rightarrow \underbrace{\left(\text{DFT}_m \otimes I_n \right)}_{\text{smp}(p,\mu)} \underbrace{T_n^{mn}}_{\text{smp}(p,\mu)} \underbrace{\left(I_m \otimes \text{DFT}_n \right)}_{\text{smp}(p,\mu)} \underbrace{L_m^{mn}}_{\text{smp}(p,\mu)} \\
 &\dots \\
 &\rightarrow \underbrace{\left((L_m^{mp} \otimes I_{n/p\mu}) \otimes I_\mu \right)}_{\left(\bigoplus_{i=0}^{p-1} T_n^{mn,i} \right)} \underbrace{\left(I_p \otimes (\text{DFT}_m \otimes I_{n/p}) \right)}_{\left(I_p \otimes (I_{m/p} \otimes \text{DFT}_n) \right)} \underbrace{\left((L_p^{mp} \otimes I_{n/p\mu}) \otimes I_\mu \right)}_{\left(I_p \otimes L_{m/p}^{mn/p} \right)} \underbrace{\left((L_p^{pn} \otimes I_{m/p\mu}) \otimes I_\mu \right)}_{\left((L_p^{pn} \otimes I_{m/p\mu}) \otimes I_\mu \right)}
 \end{aligned}$$



load-balanced, no false sharing

*One rewriting system for every platform paradigm:
SIMD, distributed memory parallelism, FPGA, ...*

Same Approach for Different Paradigms

Threading:

$$\begin{aligned}
 \underbrace{\text{DFT}_{mn}}_{\text{smp}(p,\mu)} &\rightarrow \frac{\left((\text{DFT}_m \otimes \text{I}_n) \text{T}_n^{mn} (\text{I}_m \otimes \text{DFT}_n) \text{L}_m^{mn} \right)}{\text{smp}(p,\mu)} \\
 &\dots \\
 &\rightarrow \frac{\left(\text{DFT}_m \otimes \text{I}_n \right)}{\text{smp}(p,\mu)} \frac{\text{T}_n^{mn}}{\text{smp}(p,\mu)} \frac{\left(\text{I}_m \otimes \text{DFT}_n \right)}{\text{smp}(p,\mu)} \frac{\text{L}_m^{mn}}{\text{smp}(p,\mu)} \\
 &\dots \\
 &\rightarrow \left((\text{L}_m^{mp} \otimes \text{I}_{n/p}) \otimes_\mu \text{I}_p \right) \left(\text{I}_p \otimes_\parallel (\text{DFT}_m \otimes \text{I}_{n/p}) \right) \left((\text{L}_p^{mp} \otimes \text{I}_{n/p}) \otimes_\mu \text{I}_p \right) \\
 &\quad \left(\bigoplus_{i=0}^{p-1} \text{T}_{n/p}^{mn,i} \right) \left(\text{I}_p \otimes_\parallel (\text{I}_{m/p} \otimes \text{DFT}_n) \right) \left(\text{I}_p \otimes_\parallel \text{L}_{m/p}^{mn/p} \right) \left((\text{L}_p^{mp} \otimes \text{I}_{m/p}) \otimes_\mu \text{I}_p \right)
 \end{aligned}$$

Vectorization:

$$\begin{aligned}
 \underbrace{\text{DFT}_{mn}}_{\text{vec}(v)} &\rightarrow \frac{\left((\text{DFT}_m \otimes \text{I}_n) \text{T}_n^{mn} (\text{I}_m \otimes \text{DFT}_n) \text{L}_m^{mn} \right)}{\text{vec}(v)} \\
 &\dots \\
 &\rightarrow \frac{\left(\text{DFT}_m \otimes \text{I}_n \right)^{\nu}}{\text{vec}(v)} \frac{\left(\text{T}_n^{mn} \right)^{\nu}}{\text{vec}(v)} \frac{\left(\text{I}_m \otimes \text{DFT}_n \right)^{\nu}}{\text{vec}(v)} \frac{\text{L}_m^{mn}{}^{\nu}}{\text{vec}(v)} \\
 &\dots \\
 &\rightarrow \left(\text{I}_{mn/\nu} \otimes \text{L}_{\nu}^{2\nu} \right) \left(\text{DFT}_m \otimes \text{I}_{n/\nu} \otimes \text{I}_{\nu} \right) \left(\text{T}_n^{mn/\nu} \right)^{\nu} \\
 &\quad \left(\text{I}_{m/\nu} \otimes (\text{L}_{\nu}^{\nu} \otimes \text{I}_{\nu}) \right) \left(\text{I}_{n/\nu} \otimes (\text{L}_{\nu}^{2\nu} \otimes \text{I}_{\nu}) \right) \left(\text{I}_2 \otimes \text{L}_{\nu}^{2\nu} \right) \left(\text{L}_2^{2\nu} \otimes \text{L}_{\nu} \right) \left(\text{DFT}_n \otimes \text{I}_{\nu} \right) \\
 &\quad \left(\text{L}_m^{mn} \otimes \text{I}_2 \right) \left(\text{I}_{mn/\nu} \otimes \text{L}_{\nu}^{2\nu} \right)
 \end{aligned}$$

GPUs:

$$\begin{aligned}
 \underbrace{\text{DFT}_{r,k}}_{\text{gpu}(t,c)} &\rightarrow \left(\prod_{i=0}^{k-1} \text{L}_r^{r^k} \left(\text{I}_{r^{k-i}-1} \otimes \text{DFT}_r \right) \left(\text{I}_{r^{k-i}-1} \otimes \text{T}_{r^{k-i}-1}^{r^{k-i}} \right) \text{R}_r^{r^k} \right) \text{R}_r^{r^k} \\
 &\dots \\
 &\rightarrow \left(\prod_{i=0}^{k-1} \left(\text{L}_r^{r^{i+2}/2} \otimes \text{I}_2 \right) \left(\text{I}_{r^{m-1}/2} \otimes \left(\underbrace{\text{DFT}_r \otimes \text{I}_2}_{\text{shd}(t,c)} \right) \text{L}_r^{2r} \right) \text{T}_i \right) \\
 &\quad \left(\text{L}_r^{r^{m-1}/2} \otimes \text{I}_2 \right) \left(\text{I}_{r^{m-1}/2} \otimes \left(\underbrace{\text{L}_r^{2r}}_{\text{shd}(t,c)} \right) \left(\text{R}_r^{r-1} \otimes \text{I}_r \right) \right)
 \end{aligned}$$

Verilog for FPGAs:

$$\begin{aligned}
 \underbrace{\text{DFT}_{r,k}}_{\text{stream}(r^k)} &\rightarrow \left[\prod_{i=0}^{k-1} \text{L}_r^{r^k} \left(\text{I}_{r^{k-i}} \otimes \text{DFT}_r \right) \left(\text{I}_{r^{k-i}} \otimes \text{T}_{r^{k-i}-1}^{r^{k-i}} \right) \text{L}_r^{r^k+1} \right] \text{R}_r^{r^k} \\
 &\dots \\
 &\rightarrow \left[\prod_{i=0}^{k-1} \underbrace{\text{L}_r^{r^k}}_{\text{stream}(r^k)} \left(\text{I}_{r^{k-i}} \otimes \text{DFT}_r \right) \left(\text{I}_{r^{k-i}} \otimes \text{T}_{r^{k-i}-1}^{r^{k-i}} \right) \text{L}_r^{r^k+1} \right] \text{R}_r^{r^k} \\
 &\dots \\
 &\rightarrow \left[\prod_{i=0}^{k-1} \underbrace{\text{L}_r^{r^k}}_{\text{stream}(r^k)} \left(\text{I}_{r^{k-i}-1} \otimes \text{DFT}_r \right) \left(\text{I}_{r^{k-i}-1} \otimes \text{T}_{r^{k-i}-1}^{r^{k-i}} \right) \text{L}_r^{r^k+1} \right] \text{R}_r^{r^k}
 \end{aligned}$$

- Rigorous, correct by construction
- Overcomes compiler limitations

Challenge: General Size Libraries

So far:

Code specialized to fixed input size

```
DFT_384(x, y) {
    ...
    for(i = ...) {
        t[2i] = x[2i] + x[2i+1]
        t[2i+1] = x[2i] - x[2i+1]
    }
    ...
}
```

- Algorithm fixed
- Nonrecursive code

Challenge:

Library for general input size

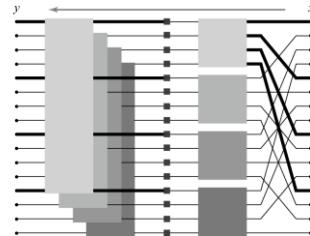
```
DFT(n, x, y) {
    ...
    for(i = ...) {
        DFT_strided(m, x+mi, y+i, 1, k)
    }
    ...
}
```

- Algorithm cannot be fixed
- Recursive code
- Creates many challenges

Challenge: Recursive Steps Needed

Cooley-Tukey FFT

$$y = (\text{DFT}_k \otimes I_m) T_m^{km} (I_k \otimes \text{DFT}_m) L_k^{km} x$$



Implementation that increases locality (e.g., FFTW 2.x)

```
void DFT(int n, cpx *y, cpx *x) {
    int k = choose_dft_radix(n);

    for (int i=0; i < k; ++i)
        DFTrec(m, y + m*i, x + i, k, 1);
    for (int j=0; j < m; ++j)
        DFTscaled(k, y + j, t[j], m);
}
```

Σ -SPL : Basic Idea

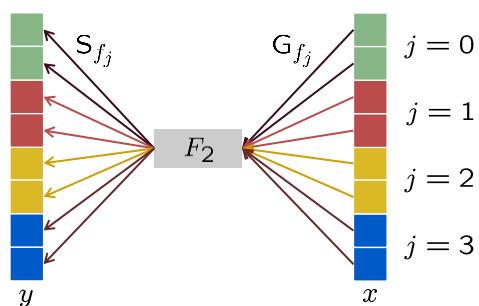
Four additional matrix constructs: Σ , G , S , Perm

Σ (sum)	matrix sum (explicit loop)
G_f (gather)	load data with index mapping f
S_f (scatter)	store data with index mapping f
Perm_f	permute data with the index mapping f

Σ -SPL formulas = matrix factorizations

Example: $y = (I_4 \otimes F_2)x \rightarrow y = \sum_{j=0}^3 S_{f_j} F_2 G_{f_j} x$

$$y = \begin{bmatrix} F_2 & & & \\ & F_2 & & \\ & & F_2 & \\ & & & F_2 \end{bmatrix} x$$



Find Recursion Step Closure

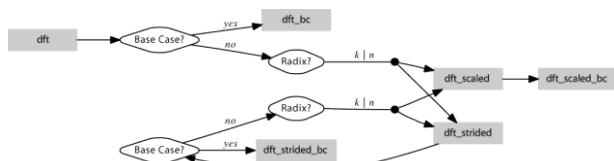
Voronenko, thesis 2008

$$\begin{aligned}
 & \{\text{DFT}_n\} \\
 & \downarrow \\
 & (\{\text{DFT}_{n/k}\} \otimes I_k) T_k^n (I_{n/k} \otimes \{\text{DFT}_k\}) L_{n/k}^n \\
 & \downarrow \\
 & \left(\sum_{i=0}^{k-1} S_{h_{i,k}} \{\text{DFT}_{n/k}\} G_{h_{i,k}} \right) \text{diag}(f) \left(\sum_{j=0}^{n/k-1} S_{h_{jk,1}} \{\text{DFT}_k\} G_{h_{jk,1}} \right) \text{perm}(\ell_{n/k}^n) \\
 & \downarrow \\
 & \sum_{i=0}^{k-1} S_{h_{i,k}} \{\text{DFT}_{n/k}\} \text{diag}(f \circ h_{i,k}) G_{h_{i,k}} \sum_{j=0}^{n/k-1} S_{h_{jk,1}} \{\text{DFT}_k\} G_{h_{j,n/k}} \\
 & \downarrow \\
 & \sum_{i=0}^{k-1} \left\{ S_{h_{i,k}} \text{DFT}_{n/k} \text{diag}(f \circ h_{i,k}) G_{h_{i,k}} \right\} \sum_{j=0}^{n/k-1} \left\{ S_{h_{jk,1}} \text{DFT}_k G_{h_{j,n/k}} \right\}
 \end{aligned}$$

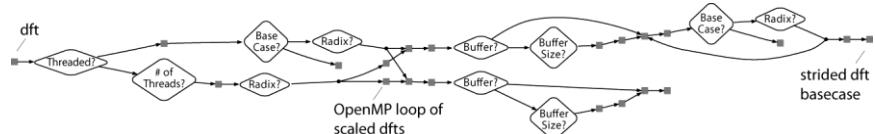
Repeat until closure

Recursion Step Closure: Examples

DFT: scalar code



DFT: full-fledged (vectorized and parallel code)

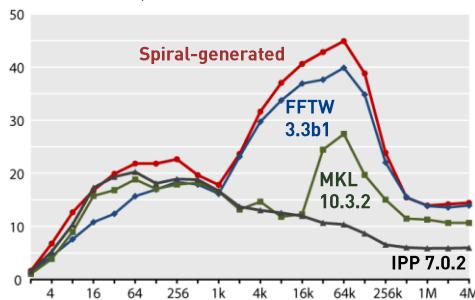


Generating Dozens of “FFTWs”

Transform	Code size	
	non-parallelized	parallelized
<i>no vectorization</i>		
DFT	13.1 KLOC / 0.59 MB	10.5 KLOC / 0.45 MB
RDFT	8.5 KLOC / 0.36 MB	8.8 KLOC / 0.39 MB
DIT	9.1 KLOC / 0.40 MB	9.4 KLOC / 0.38 MB
DCT-2	12.0 KLOC / 0.55 MB	12.4 KLOC / 0.57 MB
DCT-3	12.0 KLOC / 0.56 MB	12.3 KLOC / 0.59 MB
DCT-4	6.8 KLOC / 0.33 MB	7.1 KLOC / 0.35 MB
WHT	5.6 KLOC / 0.21 MB	
<i>2-way vectorization</i>		
DFT	14.8 KLOC / 0.73 MB	15.0 KLOC / 0.74 MB
RDFT	15.0 KLOC / 0.76 MB	16.0 KLOC / 0.81 MB
scaled RDFT	16.0 KLOC / 0.78 MB	
DIT	16.9 KLOC / 0.83 MB	17.2 KLOC / 0.87 MB
DCT-2	20.7 KLOC / 1.10 MB	21.0 KLOC / 1.09 MB
DCT-3	27.9 KLOC / 1.58 MB	28.2 KLOC / 1.59 MB
DCT-4	7.8 KLOC / 0.47 MB	8.1 KLOC / 0.50 MB
WHT	6.0 KLOC / 0.32 MB	5.8 KLOC / 0.34 MB
FIR Filter	167 KLOC / 7.75 MB	120 KLOC / 5.12 MB
Downsampled FIR Filter	100 KLOC / 4.2 MB	68 KLOC / 2.7 MB
<i>4-way vectorization</i>		
DFT	17.9 KLOC / 1.09 MB	18.2 KLOC / 1.11 MB
RDFT	8.6 KLOC / 0.88 MB	16.5 KLOC / 0.90 MB
scaled RDFT	16.5 KLOC / 0.88 MB	
DIT	17.9 KLOC / 1.02 MB	18.3 KLOC / 1.04 MB
DCT-2	23.3 KLOC / 1.50 MB	23.6 KLOC / 1.53 MB
DCT-3	32.0 KLOC / 2.17 MB	32.3 KLOC / 2.20 MB
DCT-4	8.3 KLOC / 0.63 MB	8.6 KLOC / 0.66 MB
WHT	8.5 KLOC / 0.58 MB	6.9 KLOC / 0.4 MB
2D DFT	20.6 KLOC / 1.32 MB	20.8 KLOC / 1.34 MB
2D DCT-2	27.0 KLOC / 2.1 MB	27.8 KLOC / 2.1 MB
FIR Filter	169 KLOC / 5.69 MB	74 KLOC / 3.44 MB
Downsampled FIR Filter	151 KLOC / 7.7 MB	92 KLOC / 4.61 MB

It Really Works

DFT on Sandybridge (3.3 GHz, 4 Cores, AVX)
Performance [Gflop/s]



$\text{DFT}_n \rightarrow (\text{DFT}_k \otimes I_m) T_m^n (I_k \otimes \text{DFT}_m) L_m^n$
 $\text{DFT}_n \rightarrow P_{1/2,2m}^T (\text{DFT}_{2m} \oplus (I_{k/2-1} \otimes_i C_{2m} \text{rDFT}_{2m}(i/k))) (\text{RDFT}_k \otimes I_m)$
 $\text{RDFT}_n \rightarrow (P_{1/2,2m}^T \otimes I_2) (\text{RDFT}_{2m} \oplus (I_{k/2-1} \otimes_i D_{2m} \text{rDFT}_{2m}(i/k))) (\text{RDFT}_k \otimes I_m)$
 $\text{rDFT}_{2m}(u) \rightarrow I_m^n (I_k \otimes_i \text{rDFT}_{2m}((i+u)/k)) (\text{rDFT}_{2k}(u) \otimes I_m)$

vectorized, threaded,
platform-tuned library
(5 MB source code)

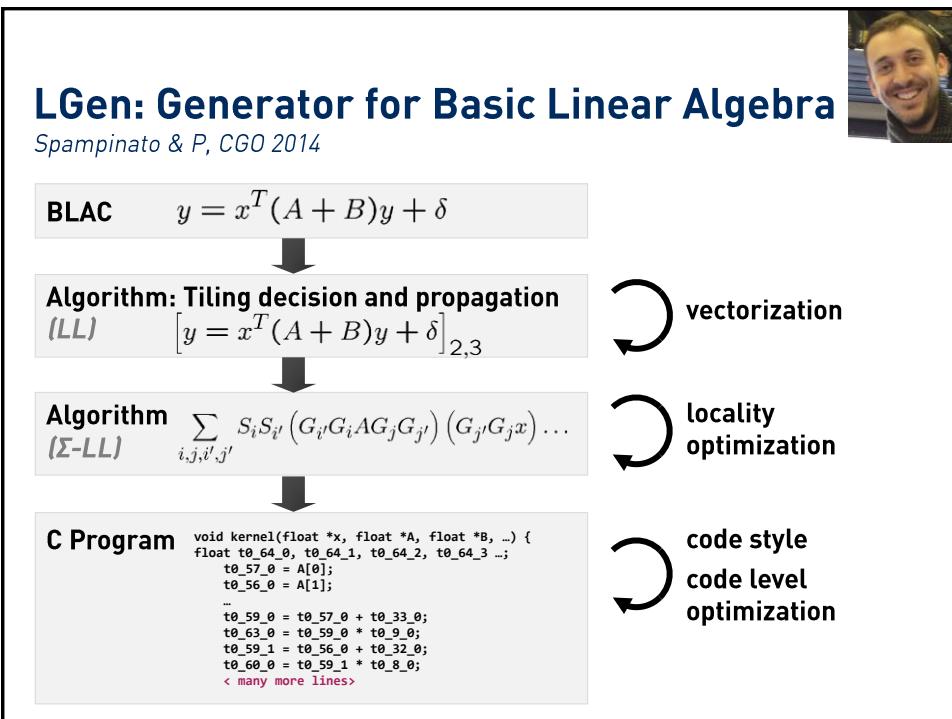
Computer generated Functions for Intel IPP

**3984 C functions
1M lines of code**

Transforms: DFT (fwd+inv), RDFT (fwd+inv), DCT2, DCT3, DCT4, DHT, WHT
Sizes: 2–64 (DFT, RDFT, DHT); 2-powers (DCTs, WHT)
Precision: single, double
Data type: scalar, SSE, AVX (DFT, DCT), LRB (DFT)

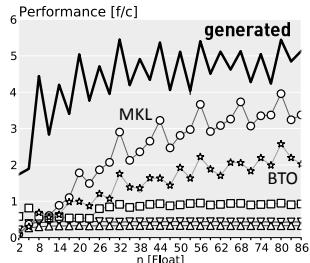
Computer generated

Results: SpiralGen Inc.

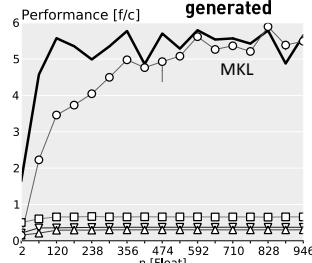


LGen: Sample Results

$$C = \alpha AB + \beta C$$



$$C = \alpha(A_0 + A_1)^T B + \beta C$$



$$A \in \mathbb{R}^{n \times 4}$$

$$B \in \mathbb{R}^{4 \times n}$$

$$A_0 \in \mathbb{R}^{4 \times 4}$$

$$B \in \mathbb{R}^{4 \times n}$$

PL Support: Example Code Style

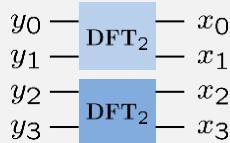
Ofenbeck, Rompf, Stojanov, Odersky & P, GPCE 2012



SPL

$$y = (\mathbf{I}_2 \otimes \text{DFT}_2)x$$

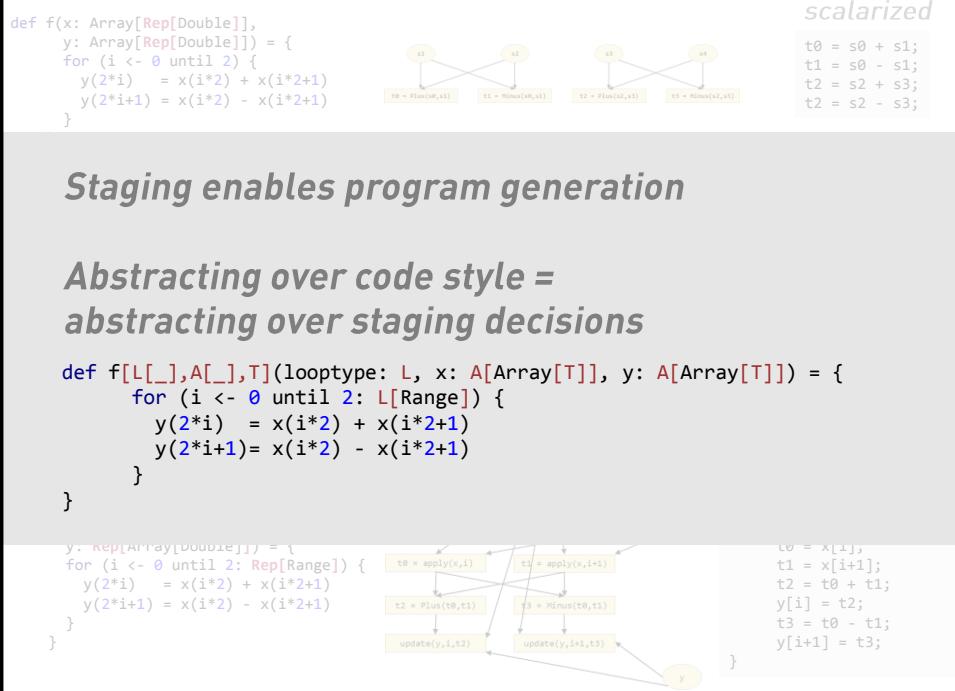
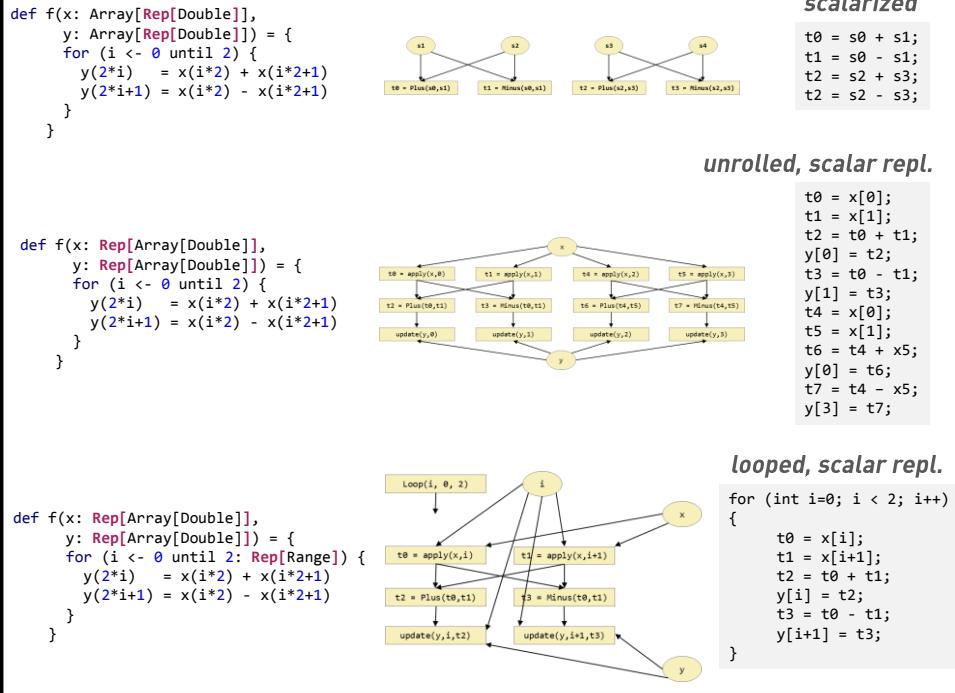
Data flow graph



Scala function

```

def f(x: Array[Double], y: Array[Double]) = {
  for (i <- 0 until 2) {
    y(2*i) = x(i*2) + x(i*2+1)
    y(2*i+1) = x(i*2) - x(i*2+1)
  }
}
  
```



Related Work

Program generators for performance

[FFTW codelet generator](#) (Frigo)

[Flame](#) (van de Geijn, Quintana-Orti, Bientinesi, ...)

[cvxgen](#) (Mattingley, Boyd)

[PetaBricks](#) (Ansel, Amarasinghe, ...)

[Spiral](#)

Autotuning

ATLAS/PhiPAC (Whaley, Bilmes, Demmel, Dongarra, ...)

FFTW adaptive library (Frigo, Johnson)

OSKI (Vuduc et al.)

Adaptive sorting (Li et al.)

Environments for DSLs and program generation

see this workshop

Automatically from Math to Fast Code

Principles

Generate Code

Capturing algorithm knowledge:
Mathematical DSLs



Structural optimization:
Rewriting

$$\begin{aligned} \text{DFT}_n &\rightarrow (\text{DFT}_k \otimes \text{I}_m) \text{ T}_m^n (\text{I}_k \otimes \text{DFT}_m) \text{ U}_k^n, \quad n = km \\ \text{DFT}_n &\rightarrow P_n(\text{DFT}_k \otimes \text{DFT}_m) Q_m, \quad n = km, \quad \gcd(k, m) = 1 \\ \text{DCT-4}_n &\rightarrow S_n \text{DCT-2}_n \text{ diag}_{0 \leq k < n} (1/(2 \cos((2k+1)\pi/4n))) \\ \text{IMDCT}_{2m} &\rightarrow (\text{J}_m \oplus \text{I}_m \oplus \text{I}_m \oplus \text{J}_m) \left(\begin{pmatrix} 1 \\ -1 \end{pmatrix} \otimes \text{I}_m \right) \oplus \left(\begin{pmatrix} -1 \\ -1 \end{pmatrix} \otimes \text{I}_m \right) \text{ J}_{2m} \text{ DCT-4}_{2m} \end{aligned}$$

Decision making:
Search and learning

$$A_m \otimes \text{I}_n \xrightarrow[\text{smp}(p, \mu)]{} \underbrace{\text{I}_m^{mn}}_{\text{smp}(p, \mu)} \left(\text{I}_p \otimes \|(\text{I}_{n/p} \otimes A_m)\right) \underbrace{\text{L}_n^{mn}}_{\text{smp}(p, \mu)}$$

Key Challenges

New domains (linear algebra, filters, ...)

Programming language support (DSLs, staging)

More information: www.spiral.net